

• General Description

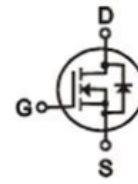
The ZMS040N10F combines advanced SGT MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

• Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

• Application

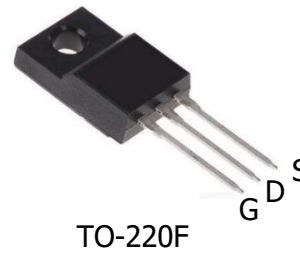
- Synchronous Rectification for AC-DC/DC-DC converter
- Oring switches
- Power Tools

• Product Summary


$V_{DS} = 100V$

$R_{DS(ON)} = 4.2m\Omega$

$I_D = 100A$


• Ordering Information:

Part NO.	ZMS040N10F
Marking	ZMS040N10
Packing Information	Bulk Tube
Basic ordering unit (pcs)	800

• Absolute Maximum Ratings ($T_C = 25^\circ C$)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_D @ TC=25^\circ C$	100	A
	$I_D @ TC=75^\circ C$	76	A
	$I_D @ TC=100^\circ C$	63	A
Pulsed Drain Current ①	I_{DM}	400	A
Total Power Dissipation	$P_D @ TC=25^\circ C$	85	W
Total Power Dissipation	$P_D @ TA=25^\circ C$	3.4	W
Operating Junction Temperature	T_J	-55 to 150	$^\circ C$
Storage Temperature	T_{STG}	-55 to 150	$^\circ C$
Single Pulse Avalanche Energy @ L=0.1mH	E_{AS}	200	mJ
Avalanche Current @ L=0.1mH	I_{AS}	35	A

•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}	-	-	2.8	° C/W
Thermal resistance, junction - ambient	R_{thJA}	-	-	62	° C/W
Soldering temperature, wave soldering for 10s	T_{sold}	-	-	265	° C

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 250\mu A$	1.3		2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 100V, V_{GS} = 0V$			1.0	μA
Gate- Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 25A$		4.2	5.2	m Ω
	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 20A$		5.0	6.5	m Ω
Forward Transconductance	g_{FS}	$V_{DS} = 25V, I_D = 10A$		28		s
Source-drain voltage	V_{SD}	$I_S = 25A$			1.28	V

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V$ $f = 1MHz$	-	4028	-	pF
Output capacitance	C_{oss}		-	1960	-	
Reverse transfer capacitance	C_{rss}		-	44	-	

•Gate Charge characteristics($T_a = 25^\circ C$)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	Q_g	$V_{DD} = 25V$	-	48	-	nC
Gate - Source charge	Q_{gs}	$I_D = 8A$	-	9.2	-	
Gate - Drain charge	Q_{gd}	$V_{GS} = 10V$	-	4.6	-	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 20A,$ $di/dt = 100A/\mu s$		TBD		nS
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F = 20A,$ $di/dt = 100A/\mu s$		TBD		nC

Note: ① Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$;

Fig.1 Gate-Charge Characteristics

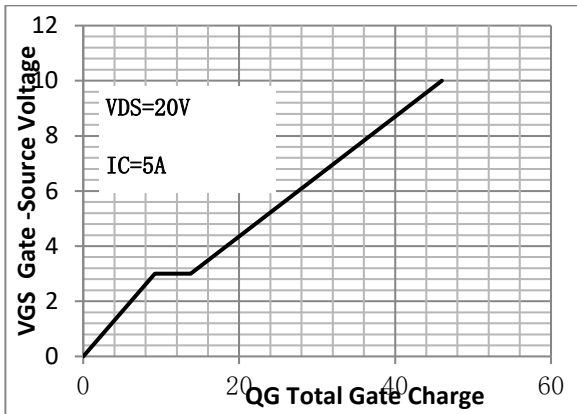


Fig.2 Capacitance Characteristics

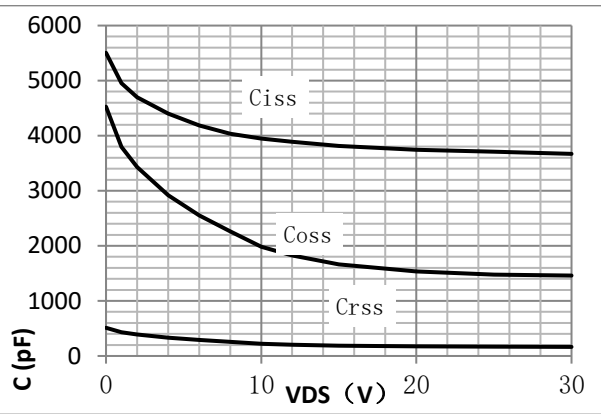


Fig.3 Power Dissipation

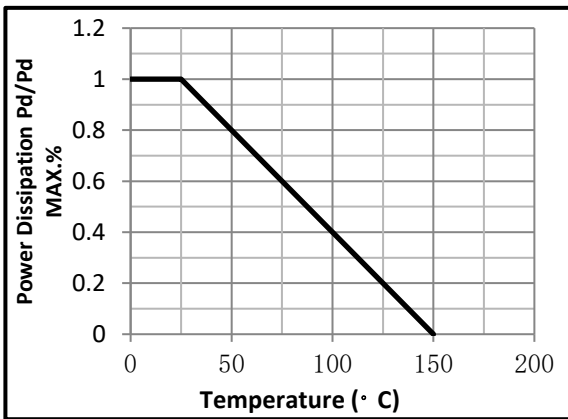


Fig.4 Typical output Characteristics

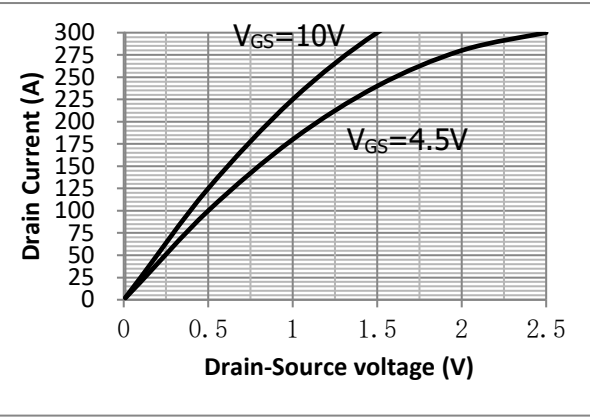


Fig.5 Threshold Voltage V.S Junction Temperature

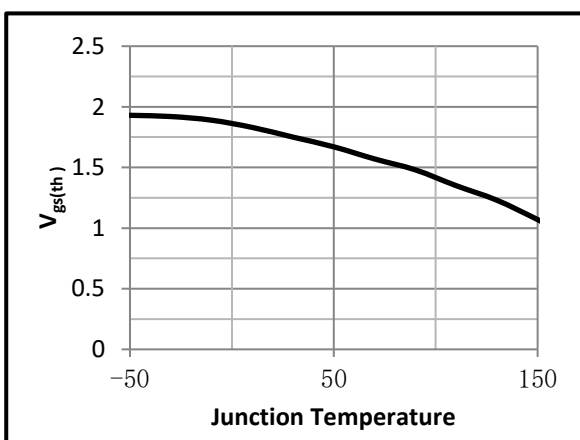


Fig.6 Resistance V.S Drain Current

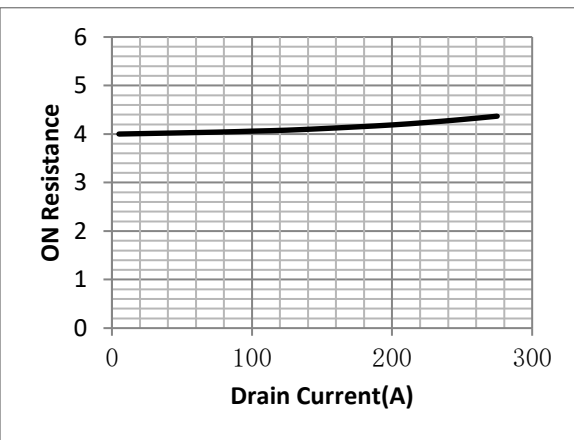


Fig.7 On-Resistance VS Gate Source Voltage

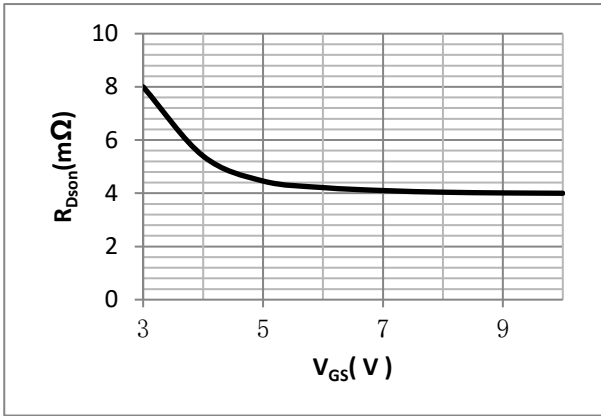


Fig.8 On-Resistance V.S Junction Temperature

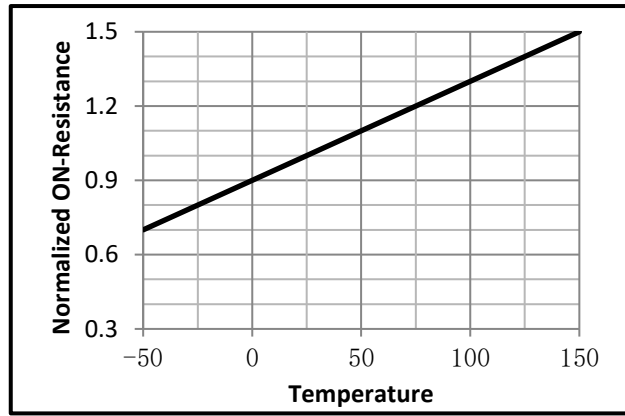


Fig.9 SOA Maximum Safe Operating Area

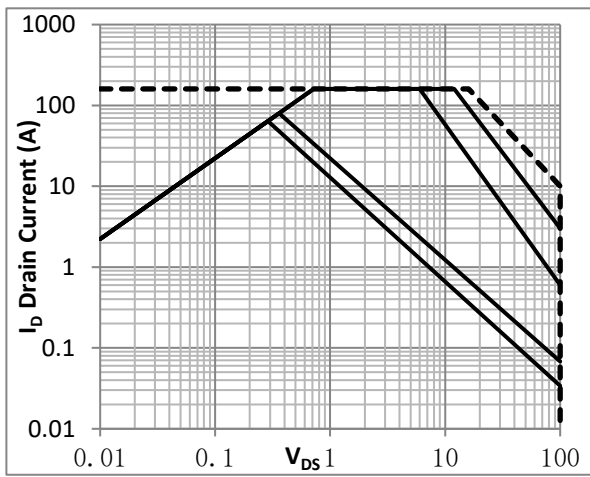


Fig.10 I_D -Junction Temperature

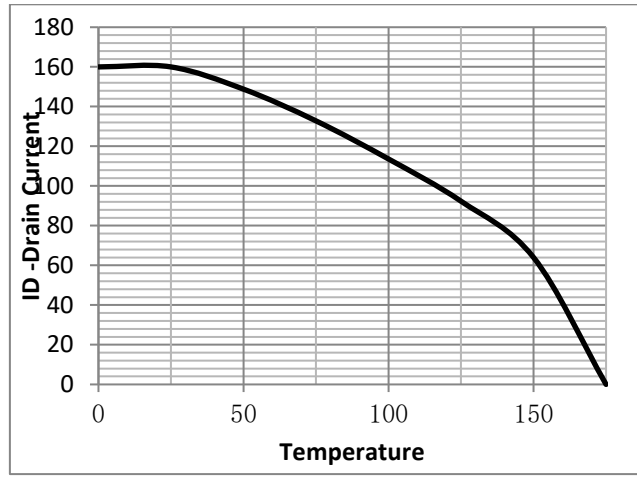


Fig.11 Switching Time Measurement Circuit

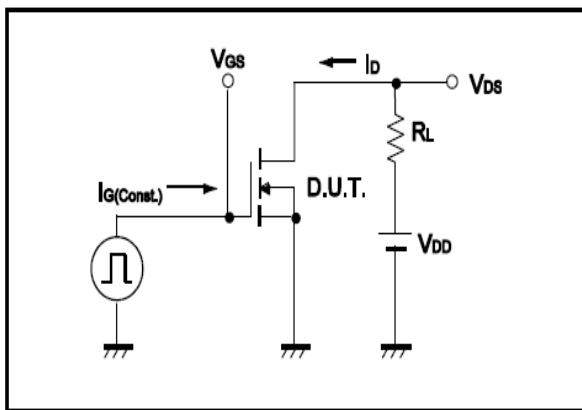


Fig.12 Gate Charge Waveform

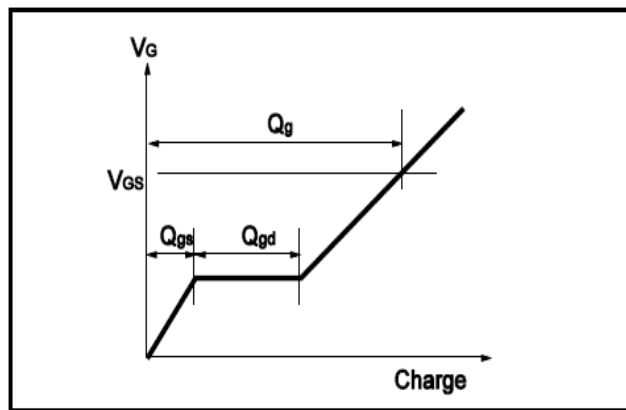


Fig.13 Switching Time Measurement Circuit

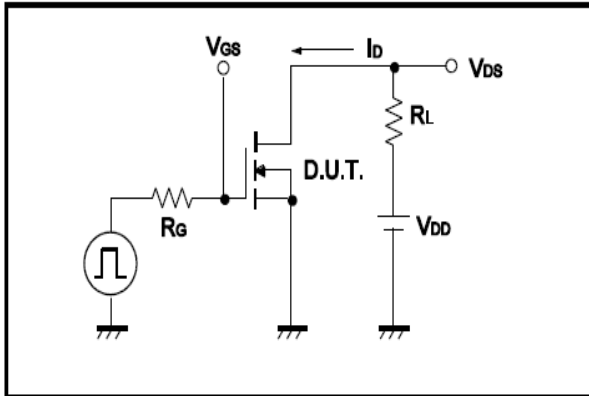


Fig.14 Gate Charge Waveform

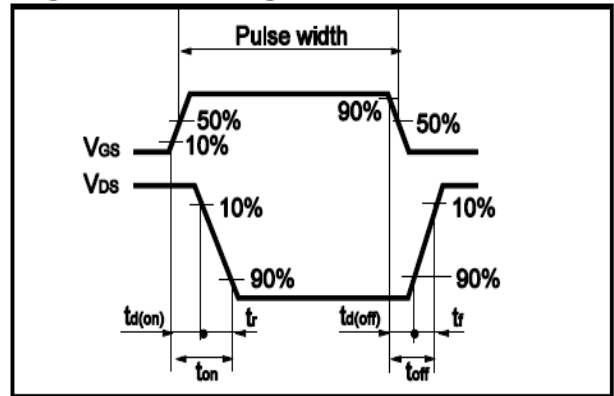


Fig.15 Avalanche Measurement Circuit

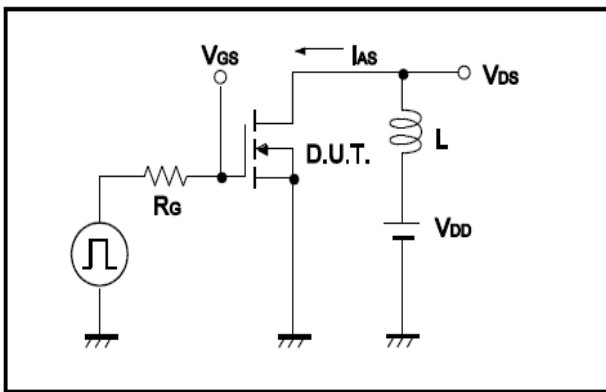
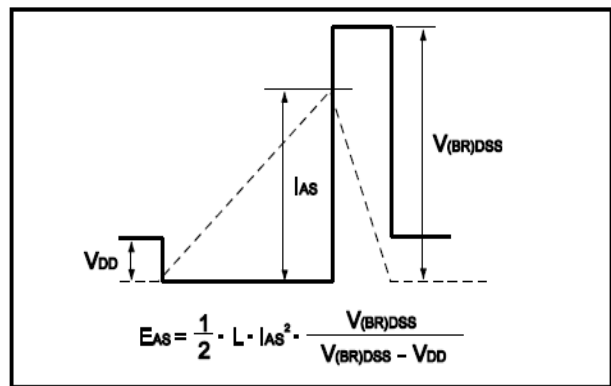


Fig.16 Avalanche Waveform





• Dimensions (TO-220F)

Unit: mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	4.40		4.95	e		2.54	
A ₁	2.30		2.90	L	12.50		14.30
b	0.45		0.90	L ₁	9.10		10.05
b ₁	1.10		1.70	L ₂	15.00		16.00
c	0.35		0.90	L ₃	3.00		4.00
D	14.50		17.00	øp	3.00		3.50
D1	6.10		9.00	Q	2.30		2.80
E	9.60		10.30				

