

### General Description

It combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ . It combines one N channel MOSFET and one P channel MOSFET.

### Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Dual DIE in one package

### Application

- Power Management in Notebook Computer
- BLDC Motor driver

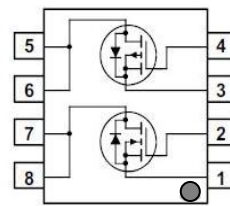
### Ordering Information:

Part NO.	ZMC88401N
Marking	ZMC88401
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

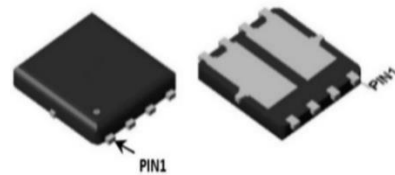
### N Channel Absolute Maximum Ratings ( $T_C = 25^\circ\text{C}$ )

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	20	V
Continuous Drain Current	$I_D @ T_C = 25^\circ\text{C}$	40	A
	$I_D @ T_C = 75^\circ\text{C}$	30	A
	$I_D @ T_C = 100^\circ\text{C}$	25	A
Pulsed Drain Current <sup>①</sup>	$I_{DM}$	120	A
Total Power Dissipation	$P_D @ T_C = 25^\circ\text{C}$	43	W
Total Power Dissipation	$P_D @ T_A = 25^\circ\text{C}$	2.2	W
Operating Junction Temperature	$T_J$	-55 to 150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 to 150	$^\circ\text{C}$
Single Pulse Avalanche Energy	$E_{AS}$	140	mJ

### Product Summary



$V_{DS1} = 40\text{V}$   
 $V_{DS2} = -40\text{V}$   
 $R_{DS(ON)1} = 8\text{m}\Omega$   
 $R_{DS(ON)2} = 12\text{m}\Omega$   
 $I_{D1} = 40\text{A}$   
 $I_{D2} = -35\text{A}$



DFN5\*6

**•P Channel Absolute Maximum Ratings (T<sub>C</sub> =25°C)**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	-40	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub> @T <sub>C</sub> =25°C	-45	A
	I <sub>D</sub> @T <sub>C</sub> =75°C	-34	A
	I <sub>D</sub> @T <sub>C</sub> =100°C	-28	A
Pulsed Drain Current ④	I <sub>DM</sub>	-135	A
Total Power Dissipation	P <sub>D</sub> @T <sub>C</sub> =25°C	43	W
Total Power Dissipation	P <sub>D</sub> @T <sub>A</sub> =25°C	2.2	W
Operating Junction Temperature	T <sub>J</sub>	-55 to 150	°C
Storage Temperature	T <sub>STG</sub>	-55 to 150	°C
Single Pulse Avalanche Energy	E <sub>AS</sub>	35	mJ

**•Thermal resistance**

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	2.9	° C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	53	° C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	° C

**•N Channel Electronic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	40			V
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2		2.5	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			1.0	uA
Gate- Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
Static Drain-source On Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =24A		8	10.4	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =12A		13	16	mΩ
Forward Trans conductance	g <sub>FS</sub>	V <sub>DS</sub> =25V, I <sub>D</sub> =10A		30		s
Source-drain voltage	V <sub>SD</sub>	I <sub>S</sub> =24A			1.28	V

**•Dynamic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
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Input capacitance	$C_{iss}$	f = 1MHz	-	1930	-	pF
Output capacitance	$C_{oss}$		-	154	-	
Reverse transfer capacitance	$C_{rss}$		-	110	-	
Total gate charge	$Q_g$	$V_{DD} = 25V$ $I_D = 15A$ $V_{GS} = 10V$	-	30	-	nC
Gate - Source charge	$Q_{gs}$		-	5.9	-	
Gate - Drain charge	$Q_{gd}$		-	5.4	-	

**•P Channel Electronic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-40			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = -250\mu A$	-1.2		-2.5	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS} = -40V, V_{GS} = 0V$			1.0	$\mu A$
Gate- Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			$\pm 100$	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS} = -10V, I_D = -3A$		12	15.6	m $\Omega$
		$V_{GS} = -4.5V, I_D = -2A$		15	19.5	m $\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS} = -10V, I_D = -2A$		18		s

**•Dynamic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	$C_{iss}$	f = 1MHz $V_{DS} = 25V$	-	3430	-	pF
Output capacitance	$C_{oss}$		-	262	-	
Reverse transfer capacitance	$C_{rss}$		-	206	-	
Total gate charge	$Q_g$	$V_{DD} = 25V$ $I_D = 5A$ $V_{GS} = 10V$	-	56	-	nC
Gate - Source charge	$Q_{gs}$		-	7.6	-	
Gate - Drain charge	$Q_{gd}$		-	10.8	-	

•N Channel characteristics curve

Fig.1 Power Dissipation Derating Curve

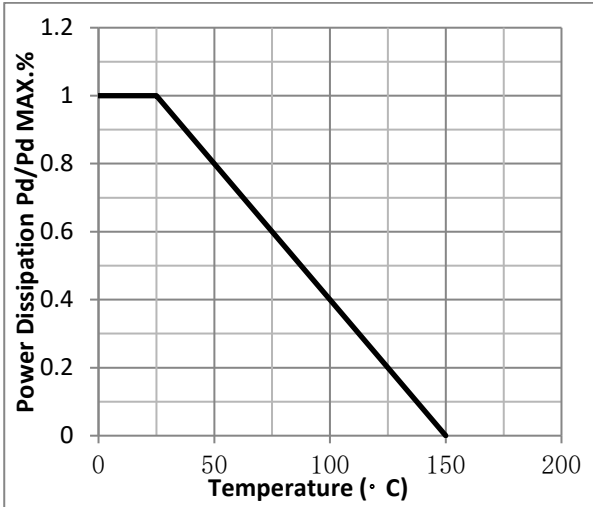


Fig.2 Typical output Characteristics

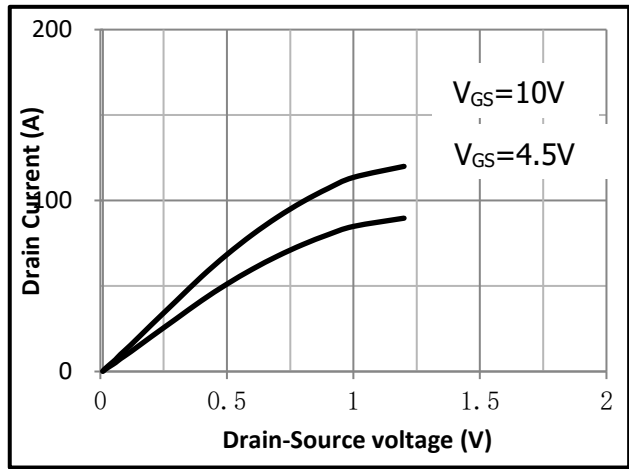


Fig.3 Threshold Voltage V.S Junction Temperature

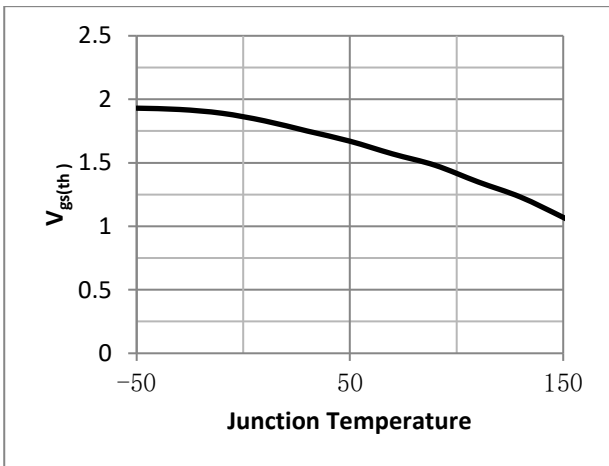


Fig.4 Resistance V.S Drain Current

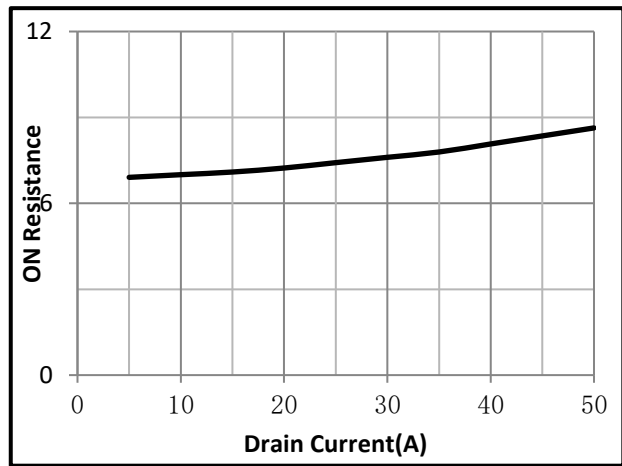


Fig.5 On-Resistance VS Gate Source Voltage

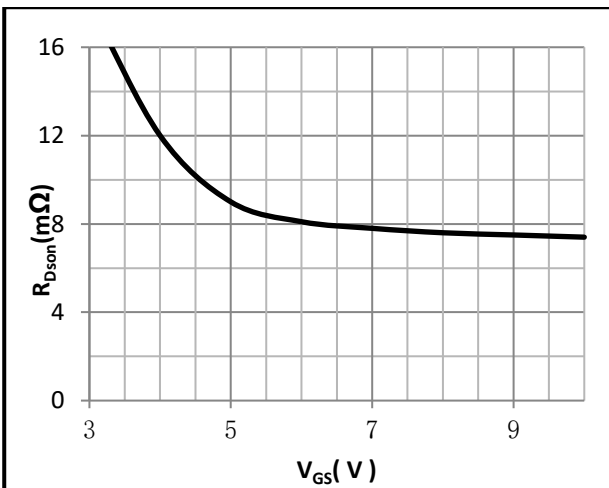
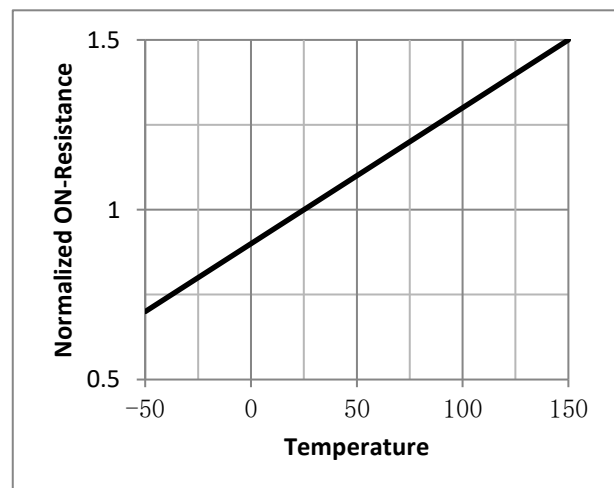


Fig.6 On-Resistance V.S Junction Temperature



•P Channel characteristics curve

Fig.1 Power Dissipation Derating Curve

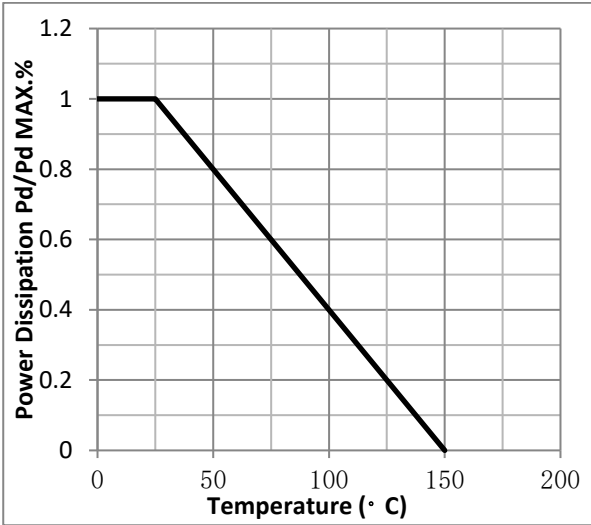


Fig.2 Typical output Characteristics

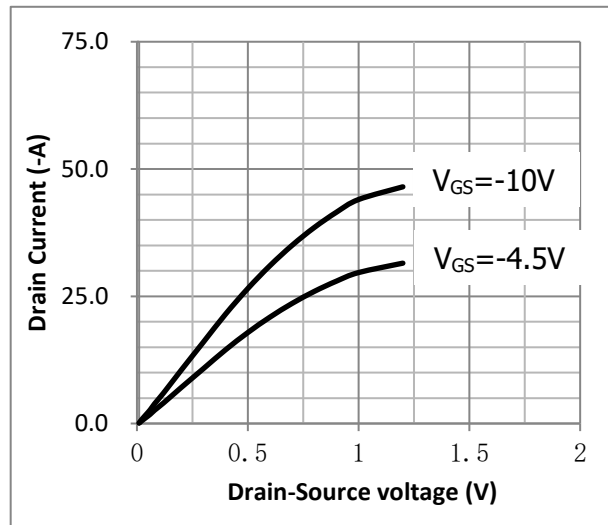


Fig.3 Threshold Voltage V.S Junction Temperature

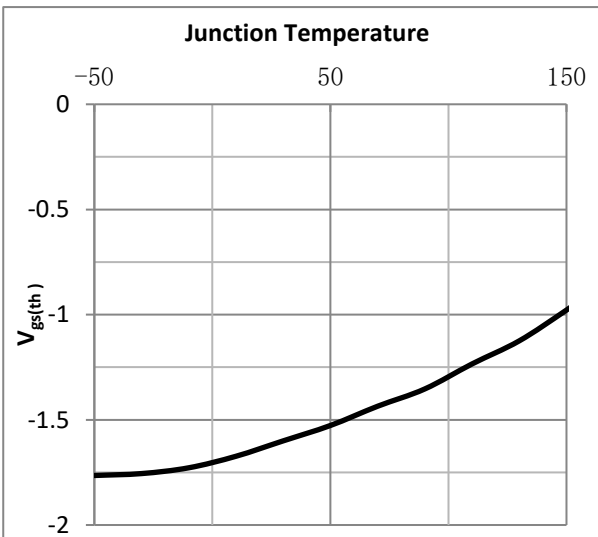


Fig.4 Resistance V.S Drain Current

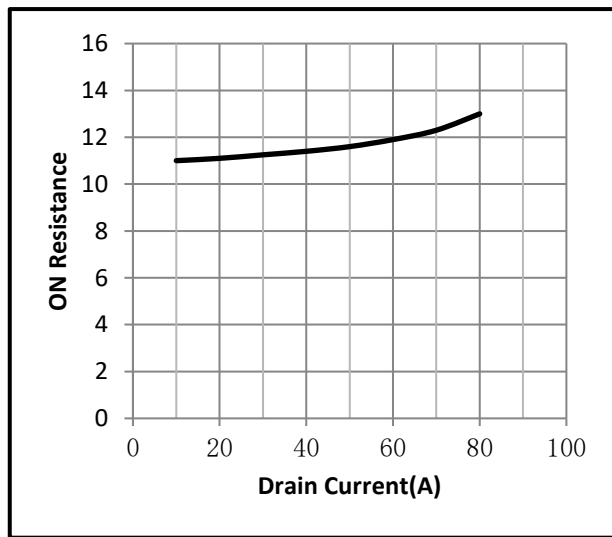


Fig.5 On-Resistance VS Gate Source Voltage

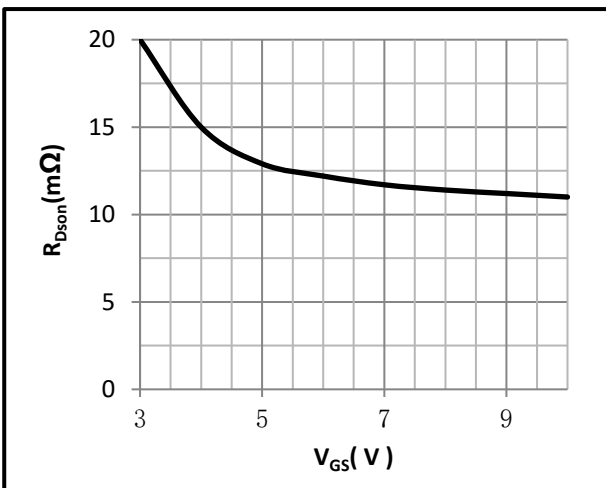
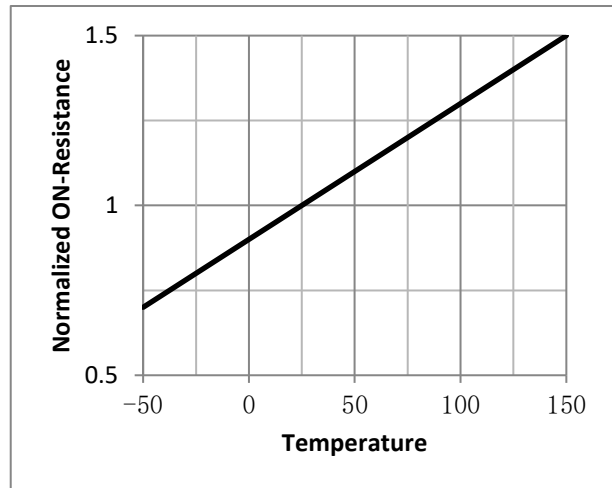


Fig.6 On-Resistance V.S Junction Temperature



•Test Circuit

Fig.1 Switching Time Measurement Circuit

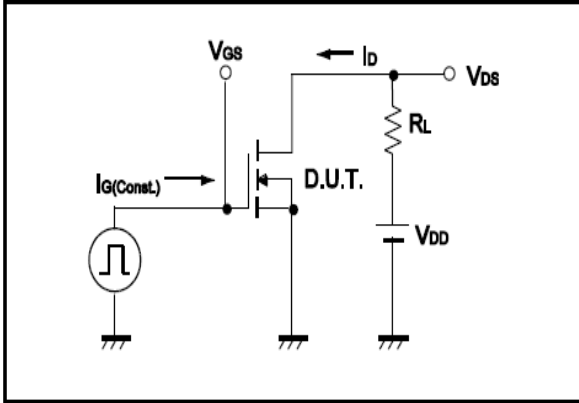


Fig.2 Gate Charge Waveform

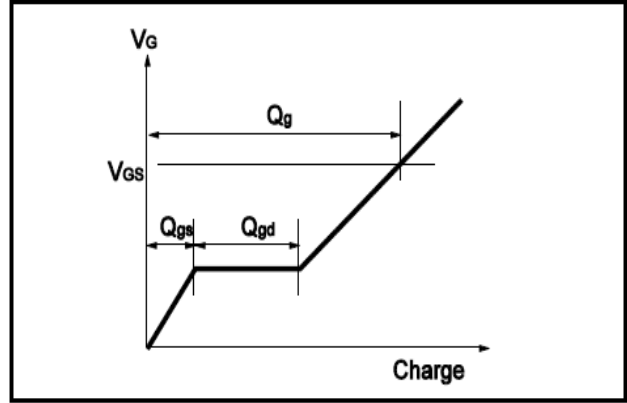


Fig.3 Switching Time Measurement Circuit

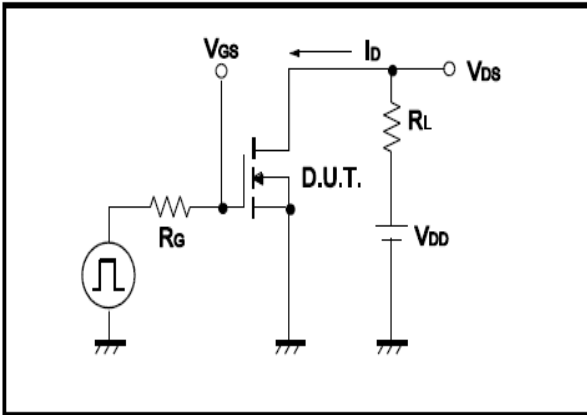


Fig.4 Gate Charge Waveform

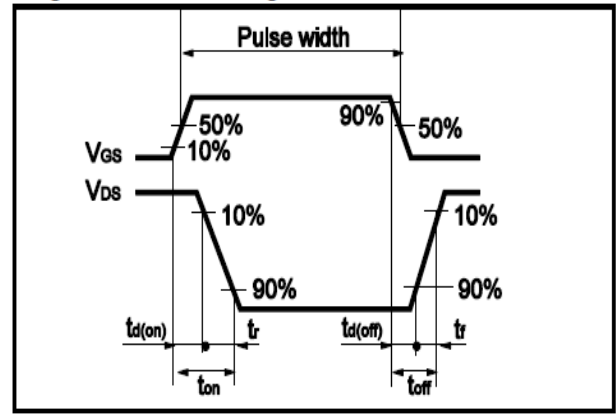


Fig.5 Avalanche Measurement Circuit

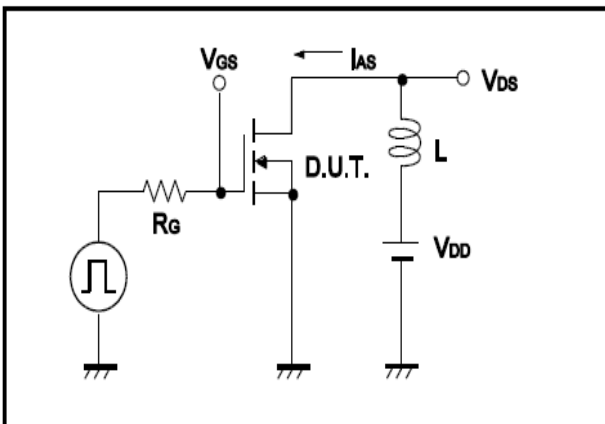
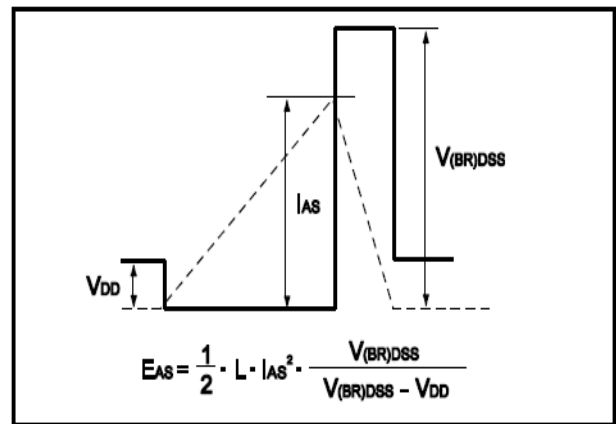
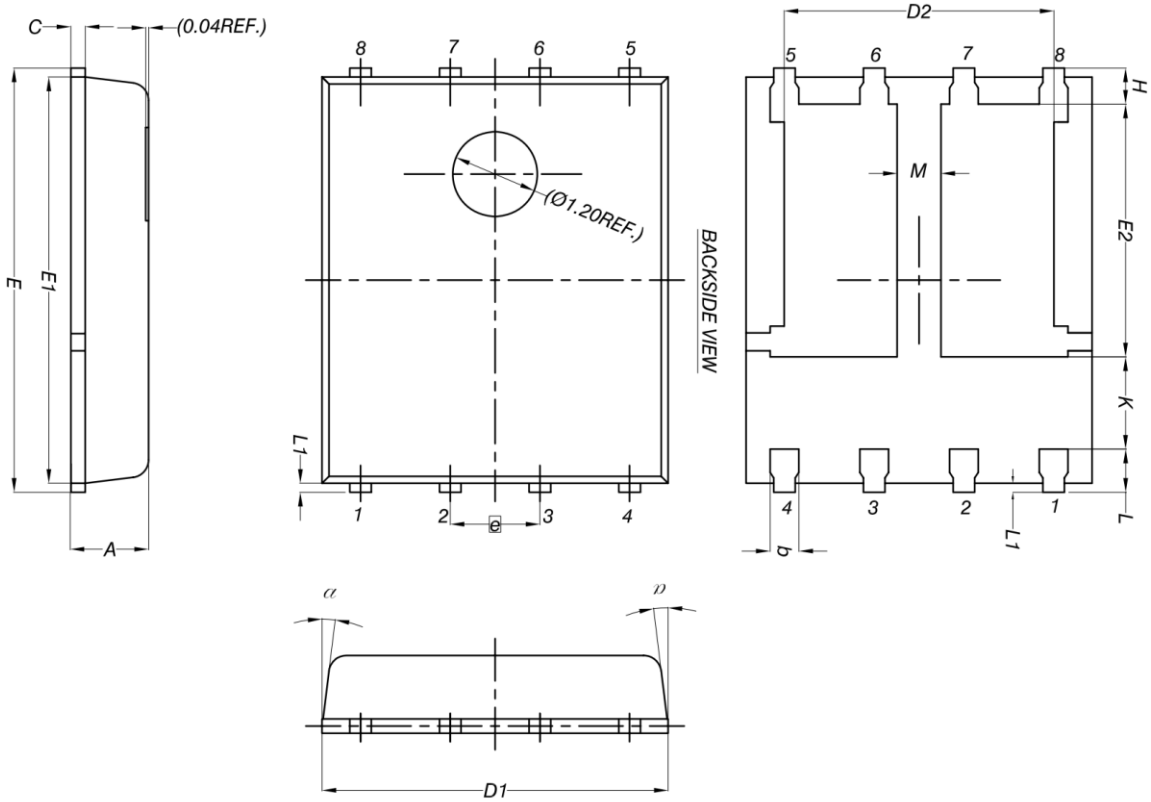


Fig.6 Avalanche Waveform





•Dimensions (DFN5x6)



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
e	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
M	0.50	-	-
α	0°	-	12°

