



**• General Description**

It combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ . This device is ideal for switch and battery protection applications.

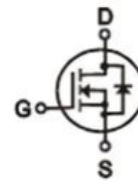
**• Features**

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- Wettable Flanks

**• Application**

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- BLDC Motor driver

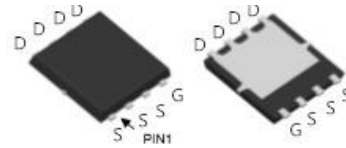
**• Product Summary**



$V_{DS} = 30V$

$R_{DS(ON)} = 3.5m\Omega$

$I_D = 77A$



DFN5 x 6

**• Ordering Information:**

Part NO.	ZMS030N03N
Marking	ZMS030N03
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

**• Absolute Maximum Ratings ( $T_C = 25^\circ C$ )**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_{D@T_C=25^\circ C}$	77	A
	$I_{D@T_C=75^\circ C}$	58	A
	$I_{D@T_C=100^\circ C}$	48	A
	$I_{D@T_A=25^\circ C}$	25	A
	$I_{D@T_A=70^\circ C}$	20	A
Pulsed Drain Current ①	$I_{DM}$	231	A
Total Power Dissipation	$P_D@T_C=25^\circ C$	40	W
Total Power Dissipation	$P_D@T_A=25^\circ C$	2.5	W
Operating Junction Temperature	$T_J$	-55 to 175	$^\circ C$
Storage Temperature	$T_{STG}$	-55 to 175	$^\circ C$



Single Pulse Avalanche Energy (L=0.5mH,VGS=10V,Rg=25Ω,TJ=25°C)	$E_{AS}$	170	mJ
Single Pulse Avalanche Energy (L=0.1mH,VGS=10V,Rg=25Ω,TJ=25°C)	$E_{AS}$	80	mJ

#### •Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}$	-	-	3.1	° C/W
Thermal resistance, junction - ambient	$R_{thJA}$	-	-	50	° C/W
Soldering temperature, wave soldering for 10s	$T_{sold}$	-	-	260	° C

#### •Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 250\mu A$	1.5		2.5	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS} = 30V, V_{GS} = 0V$			1.0	$\mu A$
Gate- Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			$\pm 100$	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 20A$		3.5	4.2	mΩ
		$V_{GS} = 4.5V, I_D = 12A$		5.0	6.5	mΩ
Forward Transconductance	$g_{FS}$	$V_{DS} = 25V, I_D = 10A$		10		s
Source-drain voltage	$V_{SD}$	$I_S = 24A$			1.28	V

#### •Dynamic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	$C_{iss}$	$f = 1MHz,$ $V_{DS} = 25V$	-	1530	-	pF
Output capacitance	$C_{oss}$		-	378	-	
Reverse transfer capacitance	$C_{rss}$		-	19	-	
Gate Resistance	$R_g$	$f = 1MHz$		2.2		Ω
Total gate charge	$Q_g$	$V_{DD} = 15V$ $I_D = 20A$ $V_{GS} = 10V$	-	29	-	nC
Total gate charge	$Q_g$ (4.5V)		-	16	-	
Gate - Source charge	$Q_{gs}$		-	3.4	-	



Gate - Drain charge	Qgd		-	7.4	-	
Turn-ON Delay time	t <sub>D(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V R <sub>G</sub> = 6Ω, I <sub>D</sub> = 30A		6.5		ns
Turn-ON Rise time	t <sub>r</sub>			3.5		ns
Turn-Off Delay time	t <sub>D(off)</sub>			26		ns
Turn-Off Fall time	t <sub>f</sub>			4.5		ns
Reverse Recovery Time	t <sub>RR</sub>	V <sub>DD</sub> = 20 V, dI <sub>S</sub> /dt = 100 A/s, I <sub>S</sub> = 30 A		17		ns
Reverse Recovery Charge	Q <sub>RR</sub>			26		nC

Note: ① Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2% ;

Fig.1 Power Dissipation

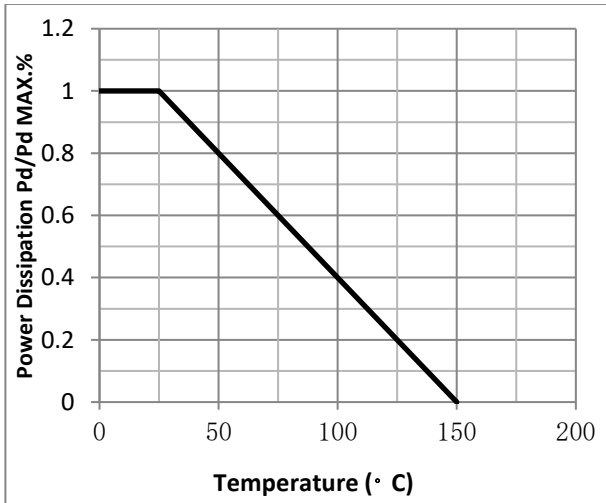


Fig.2 Typical output Characteristics

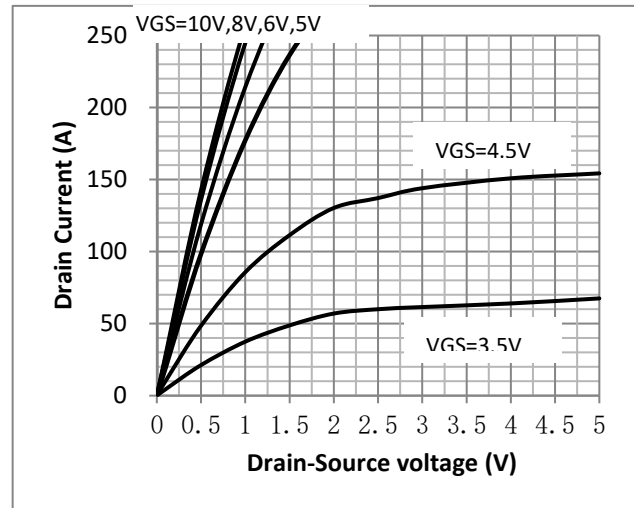


Fig.3 Threshold Voltage V.S Junction Temperature

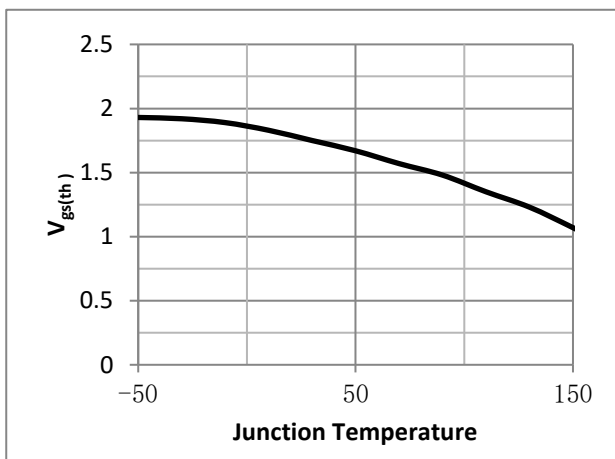


Fig.4 Resistance V.S Drain Current

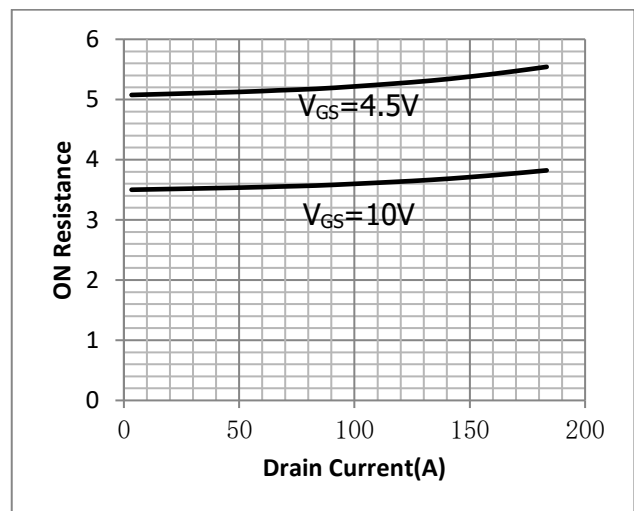




Fig.5 On-Resistance VS Gate Source Voltage

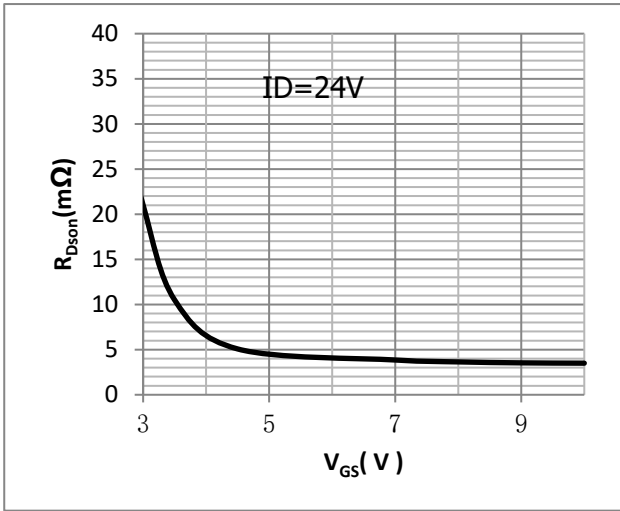


Fig.6 On-Resistance V.S Junction Temperature

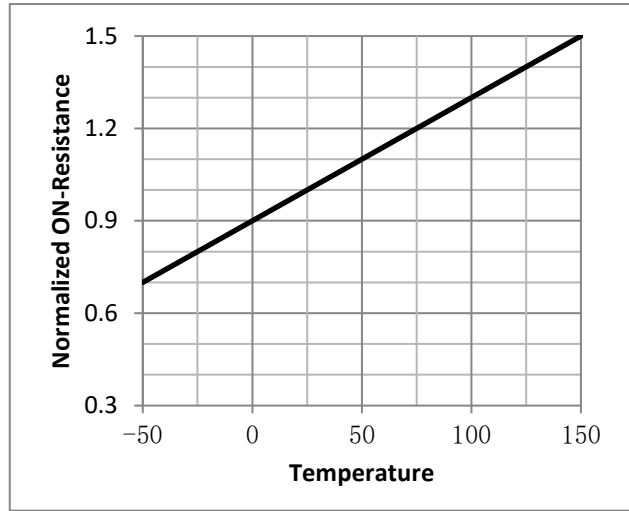


Fig.7 SOA Maximum Safe Operating Area

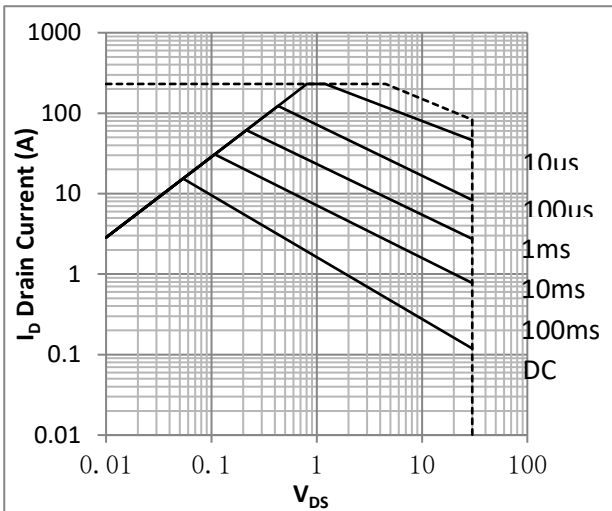


Fig.8 ID-Junction Temperature

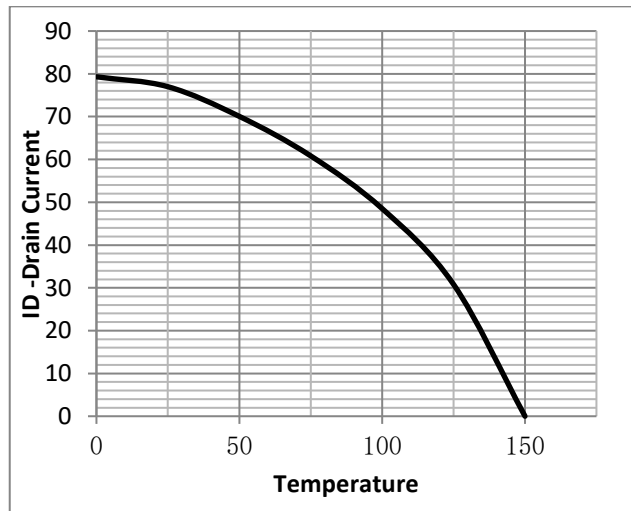


Figure 9. Diode Forward Voltage vs. Current

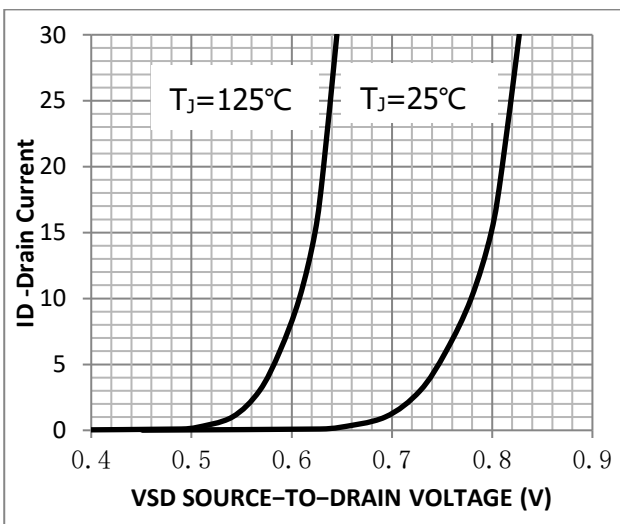


Figure 10. Transfer Characteristics

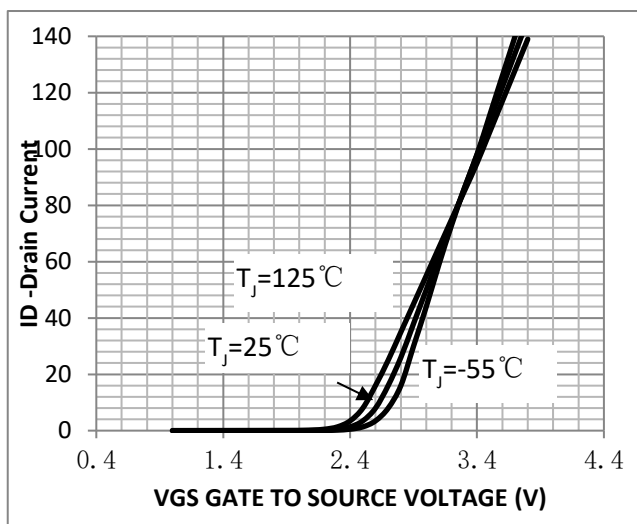




Figure 11. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

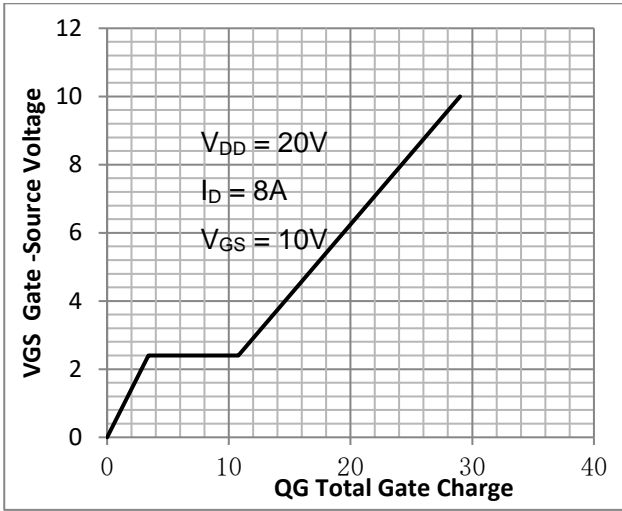


Fig.12 Capacitance Variation

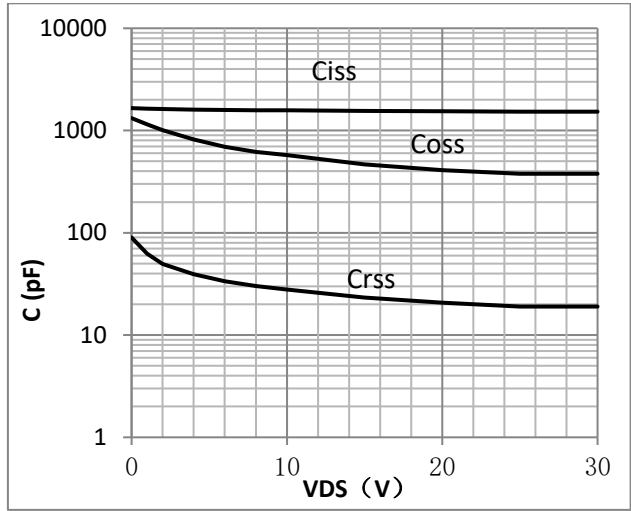


Fig.13 Normalized Maximum Transient Thermal Impedance

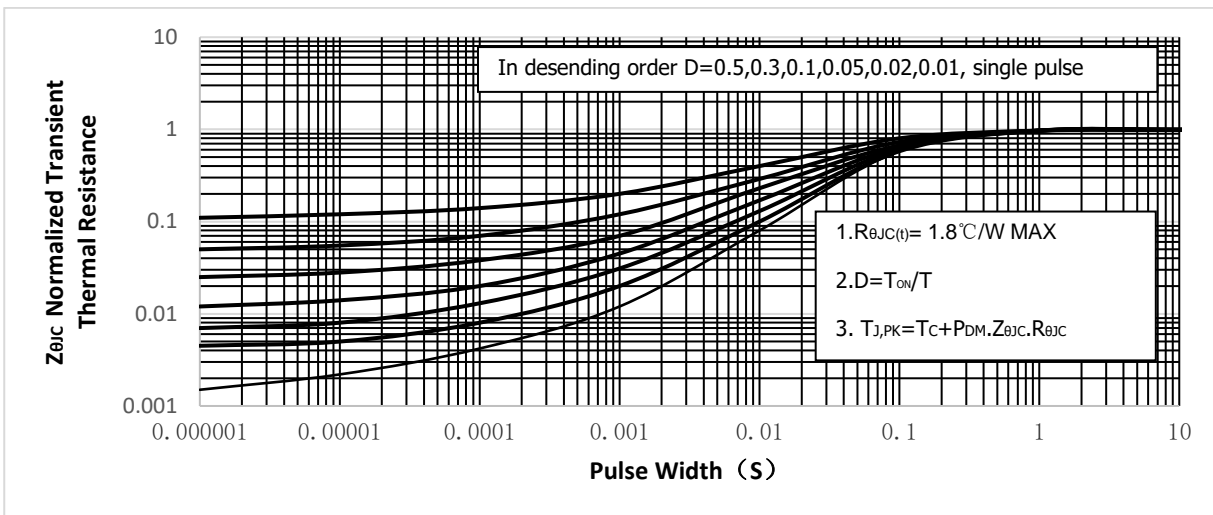


Fig.14 Switching Time Measurement Circuit

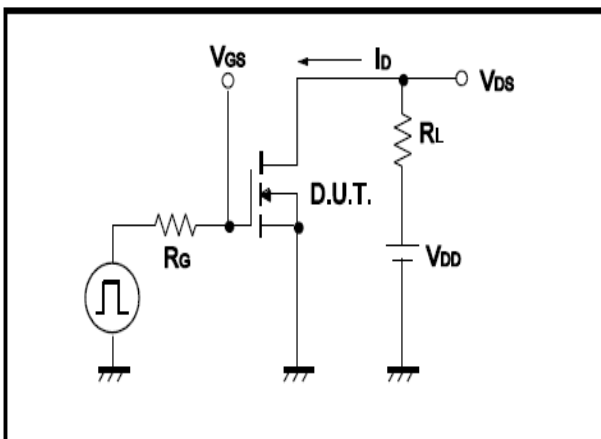


Fig.15 Gate Charge Waveform

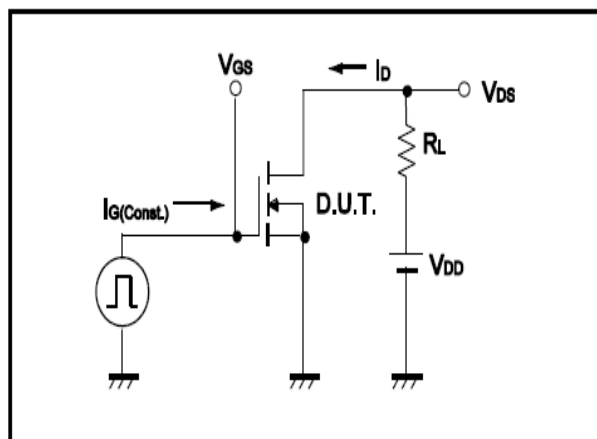




Fig.16 Avalanche Measurement Circuit

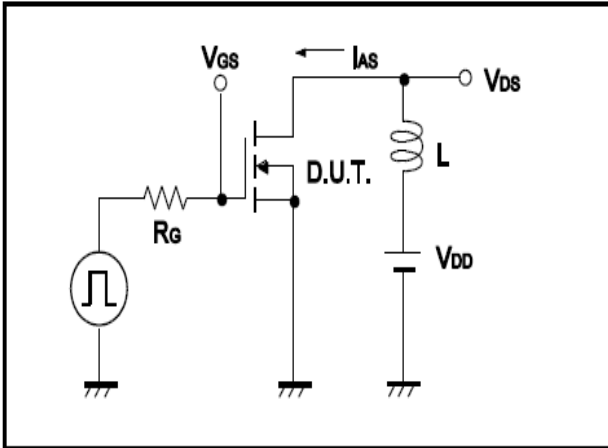


Fig.17 Avalanche Waveform

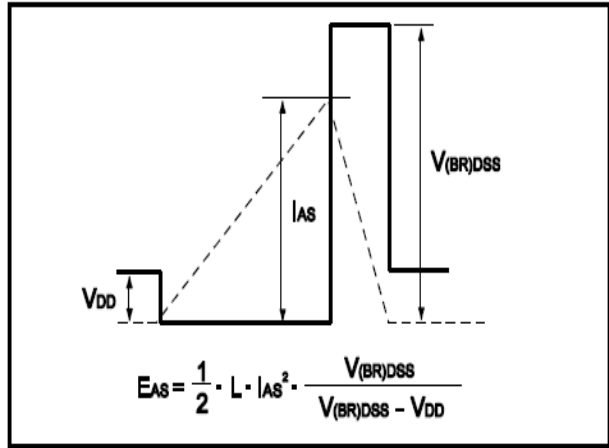
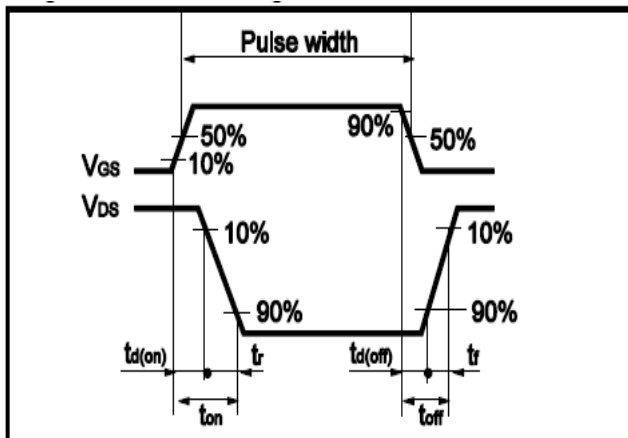


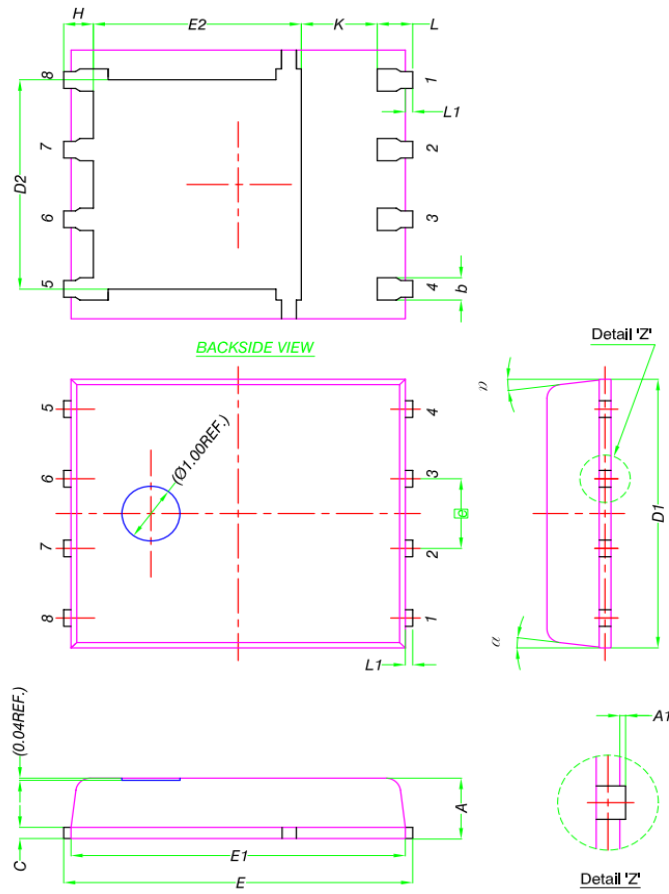
Fig.18 Gate Charge Waveform





•Dimensions (DFN5x6)

Unit: mm



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
A1	0	-	0.05
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
e	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
$\alpha$	0°	-	12°