

General Description

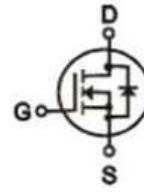
The ZM065N02D combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

Application

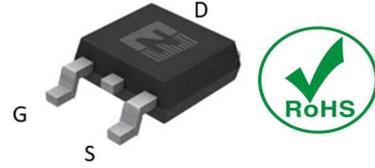
- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary


$V_{DS} = 20V$

$R_{DS(ON)} = 6.5m\Omega$

$I_D = 50A$



TO-252

Ordering Information:

Part NO.	ZM065N02D
Marking	ZM065N02
Packing Information	REEL TAPE
Basic ordering unit (pcs)	2500

Absolute Maximum Ratings (T_C = 25°C)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	±12	V
Continuous Drain Current	$I_{D@TC=25^{\circ}C}$	50	A
	$I_{D@TC=75^{\circ}C}$	38	A
	$I_{D@TC=100^{\circ}C}$	32	A
Pulsed Drain Current ^①	I_{DM}	100	A
Total Power Dissipation(TC=25°C)	$P_D@TC=25^{\circ}C$	50	W
Total Power Dissipation(TA=25°C)	$P_D@TA=25^{\circ}C$	2.0	W
Operating Junction Temperature	T_J	-55 to 150	°C
Storage Temperature	T_{STG}	-55 to 150	°C
Single Pulse Avalanche Energy	E_{AS}	70	mJ

**●Thermal resistance**

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	2.5	° C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	65	° C/W
Soldering temperature, wave soldering for 10s	T _{sold}	-	-	265	° C

●Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250uA	20			V
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = 250uA	0.3	0.65	1.0	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = 20V, V _{GS} = 0V			1.0	uA
Gate- Source Leakage Current	I _{GSS}	V _{GS} = ±12V, V _{DS} = 0V			±100	nA
Static Drain-source On Resistance	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 16A		6.5	8.4	mΩ
		V _{GS} = 2.5V, I _D = 12A		8.3	10.8	mΩ
Forward Trans conductance	g _{FS}	V _{DS} = 25V, I _D = 10A		10		s

●Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C _{iss}	V _{DS} = 15V f = 1MHz	-	1100	-	pF
Output capacitance	C _{oss}		-	230	-	
Reverse transfer capacitance	C _{rss}		-	110	-	

●Gate Charge characteristics(T_a = 25°C)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	Q _g	V _{DD} = 15V	-	12	-	nC
Gate - Source charge	Q _{gs}	I _D = 16A	-	4	-	
Gate - Drain charge	Q _{gd}	V _{GS} = 10V	-	6	-	

Note: ① Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2% ;

Fig.1 Power Dissipation

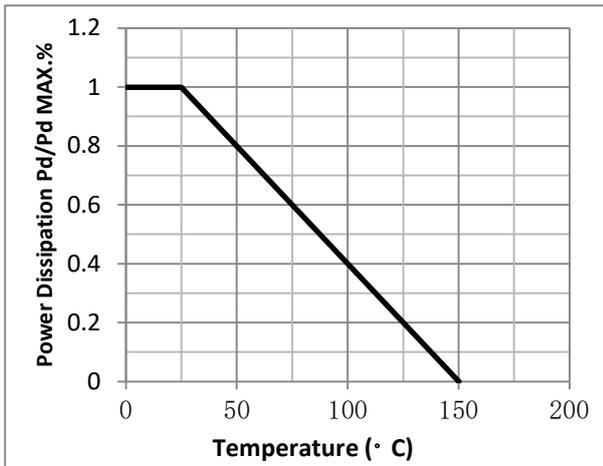


Fig.2 Typical output Characteristics

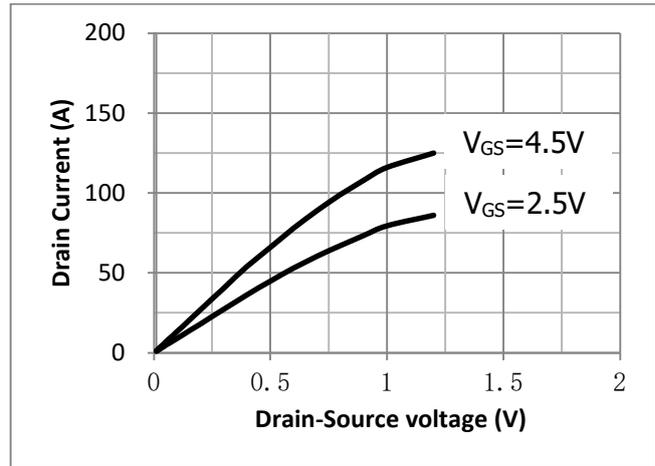


Fig.3 Threshold Voltage V.S Junction Temperature

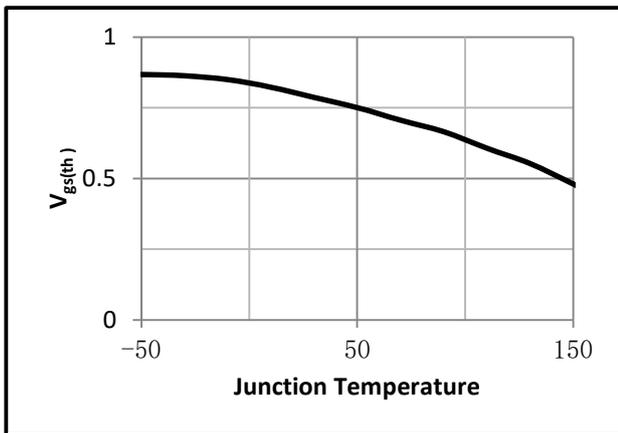


Fig.4 Resistance V.S Drain Current

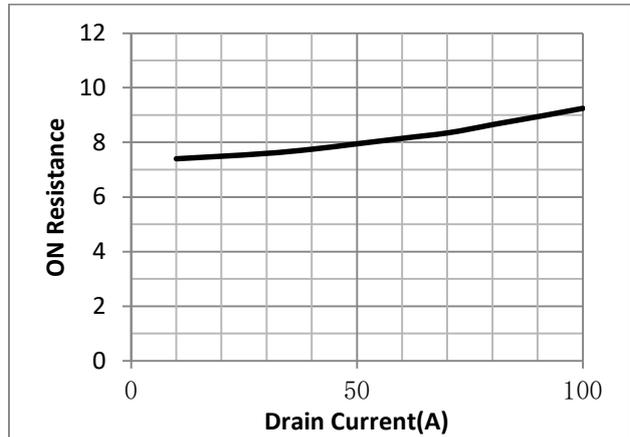


Fig.5 On-Resistance VS Gate Source Voltage

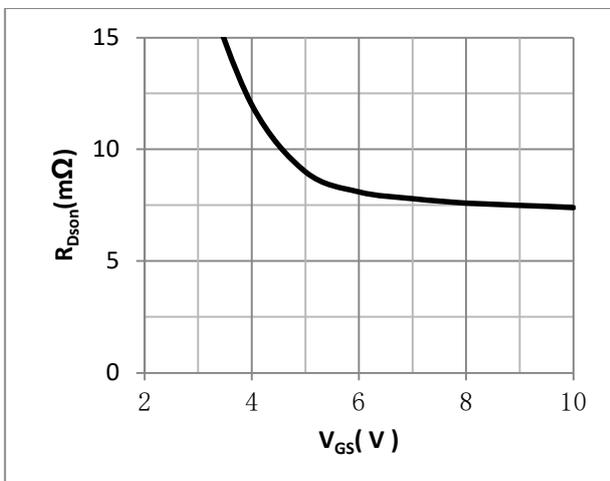


Fig.6 On-Resistance V.S Junction Temperature

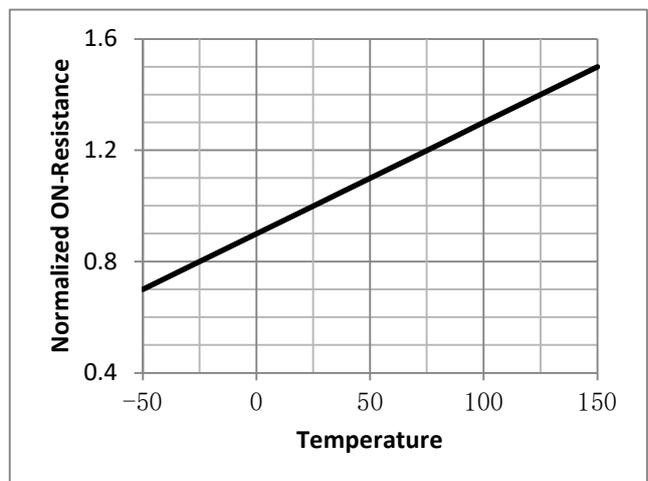


Fig.7 Gate Charge Measurement Circuit

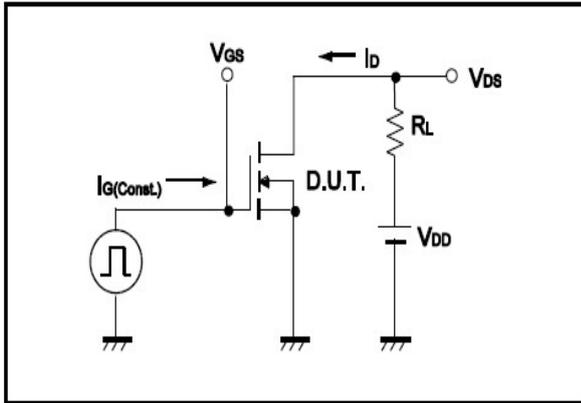


Fig.8 Gate Charge Waveform

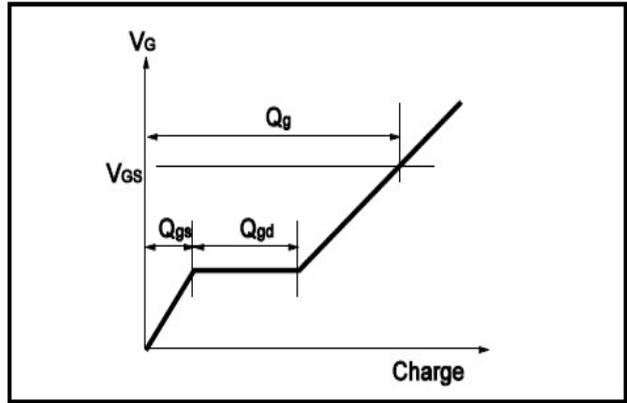


Fig.9 Switching Time Measurement Circuit

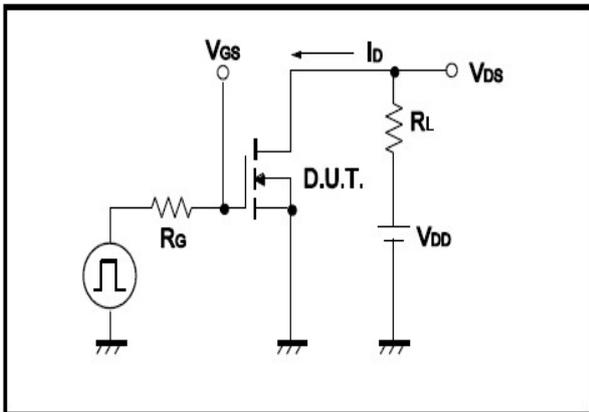


Fig.10 Switching Time Waveform

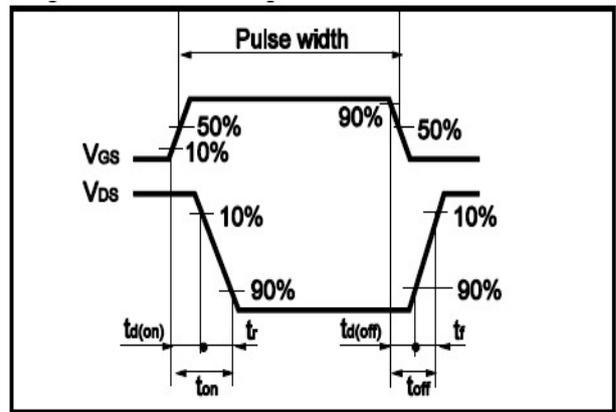


Fig.11 Avalanche Measurement Circuit

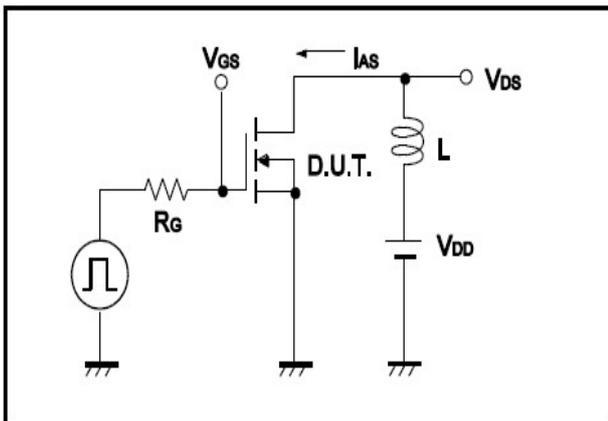
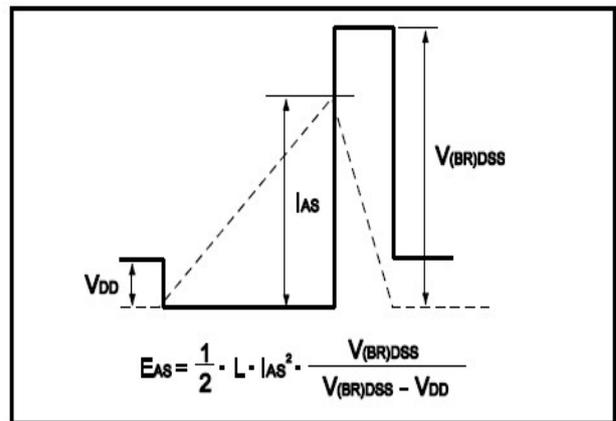


Fig.12 Avalanche Waveform





• Dimensions (TO-252)

Unit: mm

SYMBOL	min	max	SYMBOL	min	max
A	2.10	2.50	B	0.85	1.25
b	0.50	0.80	b1	0.50	0.90
b2	0.45	0.70	C	0.45	0.70
D	6.30	6.75	D1	5.10	5.50
E	5.30	6.30	e1	2.25	2.35
L1	9.20	10.60	e2	4.45	4.75
L2	0.90	1.75	L3	0.60	1.10
K	0.00	0.23			

